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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/991,338	11/13/2001	Gunawan Ali-Santosa	P-7206	3522

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EXAMINER

WILSON, YOLANDA L

ART UNIT PAPER NUMBER

2113

DATE MAILED: 06/13/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/991,338

Applicant(s)

ALI-SANTOSA ET AL.

Examiner

Yolanda Wilson

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2113

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 29 March 2005.  
2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.  
3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-10 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.  
6) ☒ Claim(s) 1-10 is/are rejected.  
7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.  
8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.  
10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All b) ☐ Some \* c) ☐ None of:  
1. ☐ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)  
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.  
4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date \_\_\_\_\_.  
5) ☐ Notice of Informal Patent Application (PTO-152)  
6) ☐ Other: \_\_\_\_\_.

**DETAILED ACTION**

***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Feldman (USPN 6038663A). As per claims 1 and 4, Feldman discloses receiving, by a processor, a message from an embedded device, said embedded device comprising a first code space comprising at least one segment and a second code space comprising one or more segments wherein said processor is included in a device different from said embedded device; decoding, by said processor, said message to determine an execution mode of said embedded device; indicating, by said processor, code in said first code space is corrupted if said execution mode indicates said embedded device is executing in said second code space; and indicating, by said processor, said code in said first code space is valid if said execution mode indicates said embedded device is executing in said first code space in column 23, lines 1-33.

3. As per claims 2 and 5, Feldman discloses receiving self-test results from said embedded device if said first code space is valid; and indicating, by said processor, code in said second code space must be restored when said self-test results indicate said code in said second code space is invalid in column 23, lines 1-18.

4. As per claims 3 and 6, Feldman discloses filtering subsequent communications from said processor to said embedded device if said execution mode indicates said embedded device is executing in said second code space in column 23, lines 1-18. The filtering is the configuration download.
5. As per claim 7, Feldman discloses a memory; a network interface coupled to said memory and configured to receive a message from an embedded device, said embedded device comprising a first code space comprising at least one segment and a second code space comprising one or more segments; a processor coupled to said network interface and configured to decode said message to determine an execution mode of said embedded device, said processor further configured to indicate code in said first code space is corrupted if said execution mode indicates said embedded device is executing in said second code space, and said processor further configured to indicate said code in said first code space is valid if said execution mode indicates said embedded device is executing in said first code space wherein said processor is included in a device different from said embedded device in column 23, lines 1-33.
6. As per claim 8, Feldman discloses The apparatus of claim 7 wherein said network interface is further configured to receive self-test results from said embedded device if said first code space is valid; and said processor is further configured to indicate code in said second code space must be restored when said self-test results indicate said code in said second code space is invalid in column 23, lines 1-18.
7. As per claim 9, Feldman discloses wherein said processor is further configured to filter subsequent communications to said embedded device if said execution mode

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indicates said embedded device is executing in said second code space in column 23, lines 1-18. The filtering is the configuration download.

8. As per claim 10, Feldman discloses examining, by a processor, a message received from a remote device to determine an execution mode of said remote device, said remote device comprising a first code space comprising at least one segment and a second code space comprising one or more segments wherein said processor is included in a device different from said remote device; and indicating validity of code in said first code space based on whether said execution mode indicates said remote device is executing in said second code space in column 23, lines 1-33.


***Response to Arguments***

9. Applicant's arguments filed 03/29/2005 have been fully considered but are moot in view of the added limitation to the independent claims. Claims 1-10 have been rejected using a new reference, Feldman, as indicated in the above disclosed rejection.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yolanda Wilson whose telephone number is (571) 272-3653. The examiner can normally be reached on M-F (7:30-4:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Beausoliel can be reached on (571) 272-3645. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
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